Fig.1
Background Art

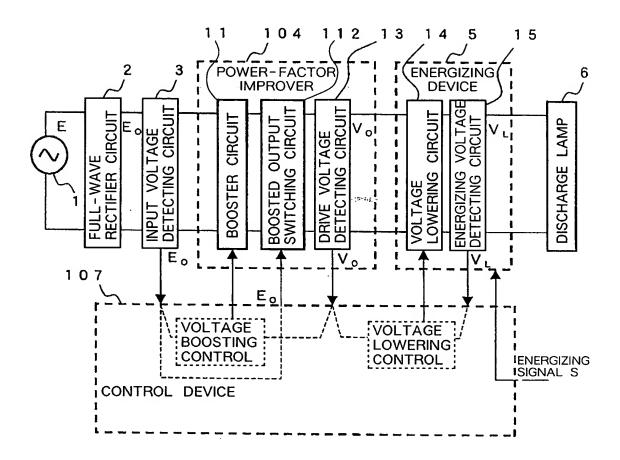
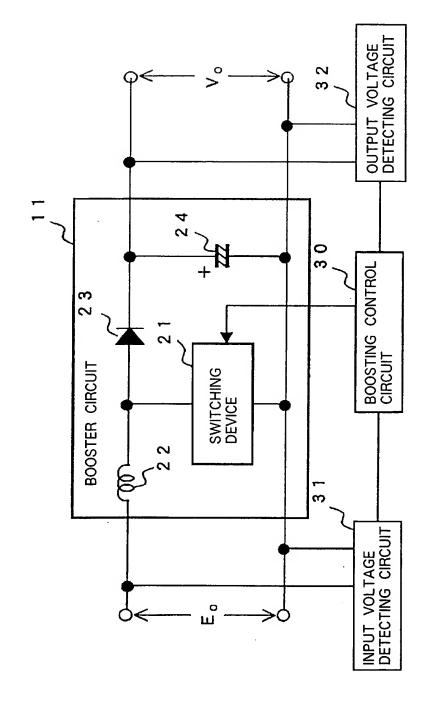


Fig.2

Background Art



OUTPUT VOLTAGE DETECTING CIRCUIT 52 4 4 Background Art Fig.3 VOLTAGE LOWERING CIRCUIT 5 0 VOLTAGE LOWERING CONTROL CIRCUIT SWITCHING DEVICE INPUT VOLTAGE DETECTING CIRCUIT

Fig.4

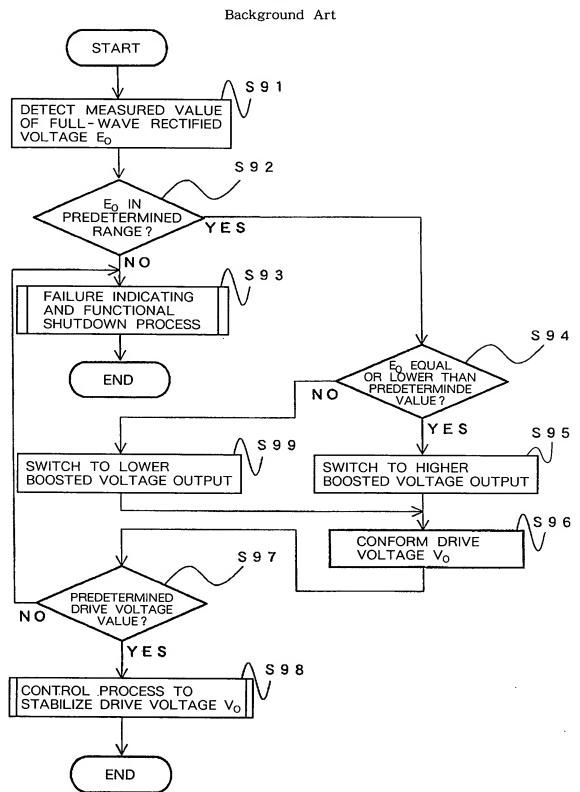


Fig.5

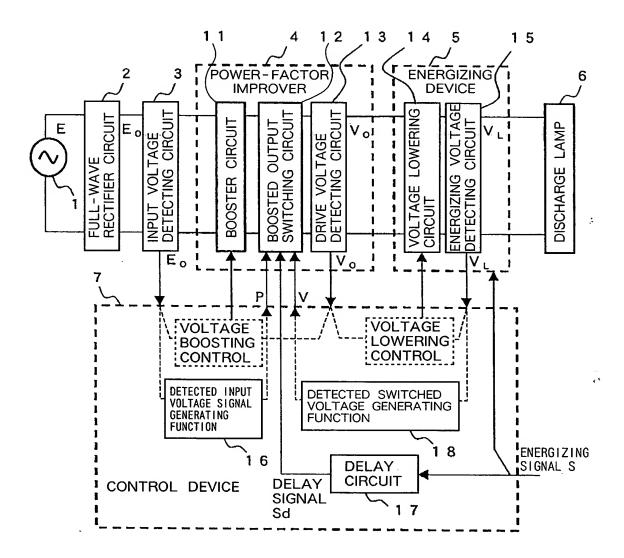


Fig.6

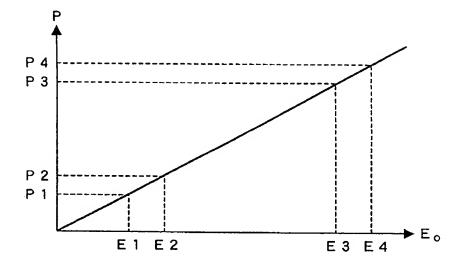


Fig.7

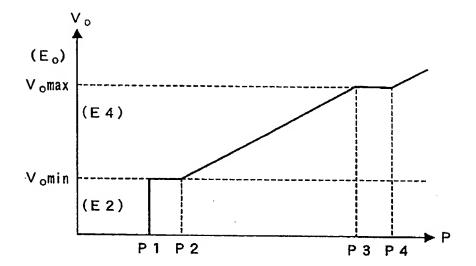


Fig.8

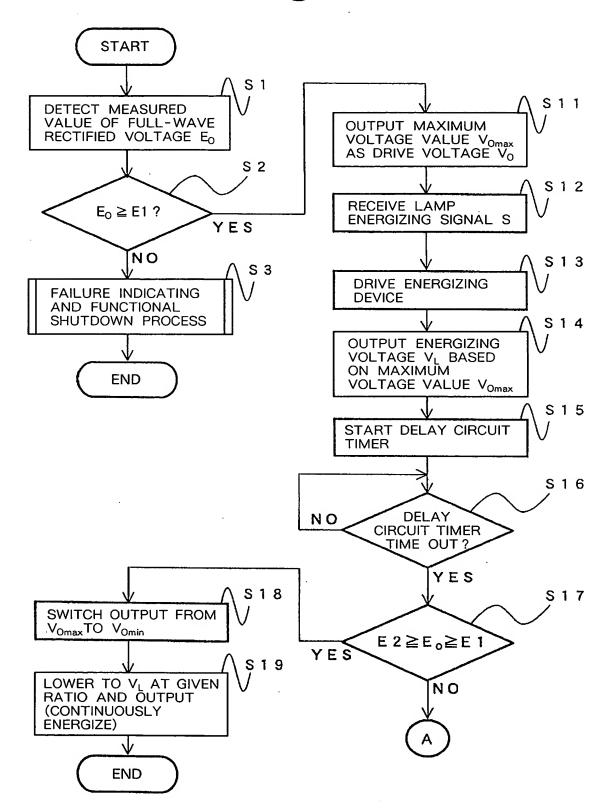


Fig.9

